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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/813,348	03/29/2004	Yet-Zen Lin	70272-010701	5009	
33717	7590	05/01/2007			
GREENBERG TRAURIG LLP				EXAMINER	
2450 COLORADO AVENUE, SUITE 400E				ZUBAJLO, JENNIFER L	
SANTA MONICA, CA 90404				ART UNIT	
				PAPER NUMBER	
				2609	
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)
	10/813,348	LIN ET AL.
Examiner	Art Unit	
Jennifer Zubajlo	2609	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 19 April 2007.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-10 is/are pending in the application.
 - 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-10 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Drawings

1. Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-12 are rejected under 35 U.S.C. 102(e) as being anticipated by Tong et al (Pub. No.: US 2002/0186329) hereinafter Tong.

3. For claim 1, Tong teaches:

An LCD for use in a particular region comprising: an electronics board (circuit board – see figure 4 item 406) within a housing of the LCD (see figures 1, 2, and 4 and [0017]); at least one access slot to the electronics board from outside of the housing (see figure 3 – item 306); and at least one insertable module which mates with the electronics board through the at least one access slot (dock) (see figure 3 – item 304 and 306); wherein the at least one insertable module is configured for a particular region and comprises at least one of a tuner, an extended audio and video connector, and a power supply (see figures 1, 2, 3, and 4, [0018], [0020], [0024]).

For claims 2-4 (dependent on claim 1), note figures 1 (item 113) and 4 (item 406 – main circuit/electronics board), [0020] and [0030] which establish that the module can be any one of a many modules that has a specific operation, such as providing the display with PAL, SECAM, or NTSC tuning capabilities which correspond to specific geographic regions to accommodate the respective mode.

For claims 5 and 6 (dependent on claim 1), note figure 1 (item 102), [0018] and [0030] teach a power supply from dock (item 102) or additional external module ([0018]), and well known video standards like NTSC (which requires 110 V AC at 60 Hz) and PAL (which require 230 V AC at 50 Hz) are accommodated.

For claim 7 (dependent on claim 1), note figure 1 (item 114) and [0033] teach the extended audio-video connector module configured for use with at least one of an S-video input, a VGA input, a SCART input, a component video input, a composite video input, an optical audio input, a coaxial audio input, and an RCA audio input.

For claim 8, Tong teaches:

A circuit board assembly of an LCD (see [0017]), comprising: a mother (main circuit) board (see figure –item 406) including (i) an audio-video decoder (see [0024]), (ii) a connector interface for receiving at least one analog signal from an external source, (iii) an analog to digital converter for converting the at least one analog signal into an at least one digital signal (see Abstract, [0009], [0016], [0017], [0032], [0033], and [0036]) , (iv) a scaler for reconverting the at least one digital signal into an at least one video signal (see [0009], [0016], [0017], [0032], [0033], and [0036]), and (v) an LCD panel for receiving the at least one video signal from the scaler for displaying images thereon; an insertable power supply module for generating power to operate the motherboard, said power supply module being configured to operate at a power supply rating specific to a region where the LCD is to be operated; an insertable audio-video interface module comprising audio-video input-output connectors for receiving audio-video signals from the mother board or transmitting the same to the mother board (see Abstract, figure 1 – items112-116, [0009], [0016], [0017], [0032], [0033], and [0036]); and an insertable tuner module for receiving the TV signal and transmitting the same to the audio-video decoder, wherein the tuner module is configured for operation in the region (see [0030] wherein PAL, NTSC, and SECAM tuners operate according to designated geographic regions).

For claims 9 and 10 (dependent on claim 8), external sources include a computer (see figure 1 – item 112 and [0019]) and a video game console system (see figure 1 – item 116 and [0023]).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer Zubajlo whose telephone number is (571) 272-2222. The examiner can normally be reached on Monday-Friday, 8 am - 5 pm, EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amare Mengistu can be reached on (571) 270-1550. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Jennifer Zubajlo



AMARE MENGISTU
SUPERVISORY PATENT EXAMINER